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APPLICATION NO FILING			G DATE FIRST NAMED			INVENTOR	ATTORNEY DOCKET NO. CONFIRMATION NO.					
10/770,479 2/4/		/2004	_1	Masaki Ito		<del></del>	NITT.0189		1890			
TITLE OF INVENTION: DESIGN METHOD AND APPARATUS FOR A SEMICONDUCTOR INTEGRATED CIRCUIT COMPRISING CHECKERS												
VERIFYING THE INTERFACE BETWEEN CIRCUIT BLOCKS												
APPLN. TYPE	YPE SMALL ENTITY		ISSUE FEE		PUBLICATION FEE		TOTAL F	FEE(S) DUE	DATE DUE			
nonprovisional NO		\$1,400		\$300		\$1,70	51,700 9/21/2006					
EXAMINER			ART UNIT	ART UNIT CLASS - SUBCLASS								
THOMPSON, AN	2825 716-018000											
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Hitachi, Ltd.								Tokyo, Japan				
Please check the appropriate assignee category indicated below (will not be printed on the patent)							Corporation	or private group entit	y 🛮 governm	nent		
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□ Publication Fee     □ A check in the amount of the fee(s) is end									ee(s) is enclosed	l.		
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